

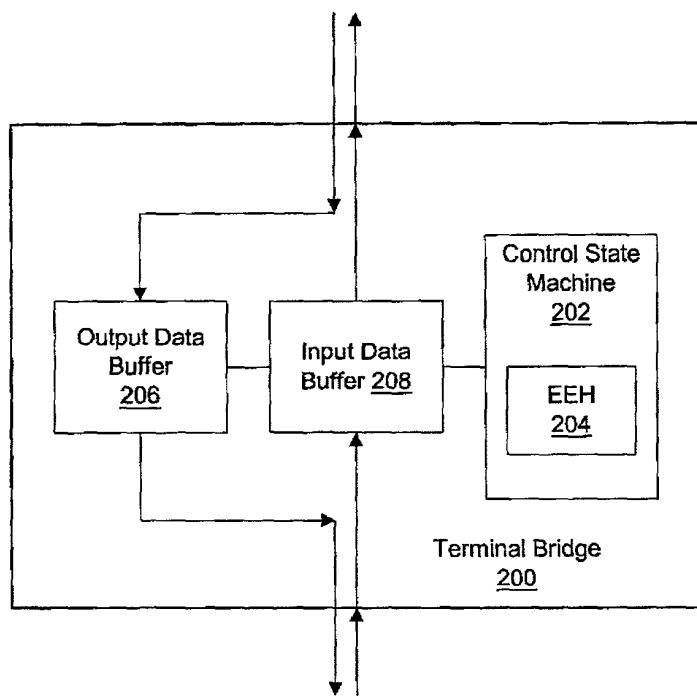
Figure 1

Arndt et al.
 AUS920010142US1
 Method and Apparatus for Isolating Failing
 Hardware in a PCI Recoverable Error
 Page 1 of 4

Figure 2

Arndt et al.
AUS920010142US1
Method and Apparatus for Isolating Failing
Hardware in a PCI Recoverable Error
Page 2 of 4

To & From PCI Host Bridge



To & From PCI I/O Adapter

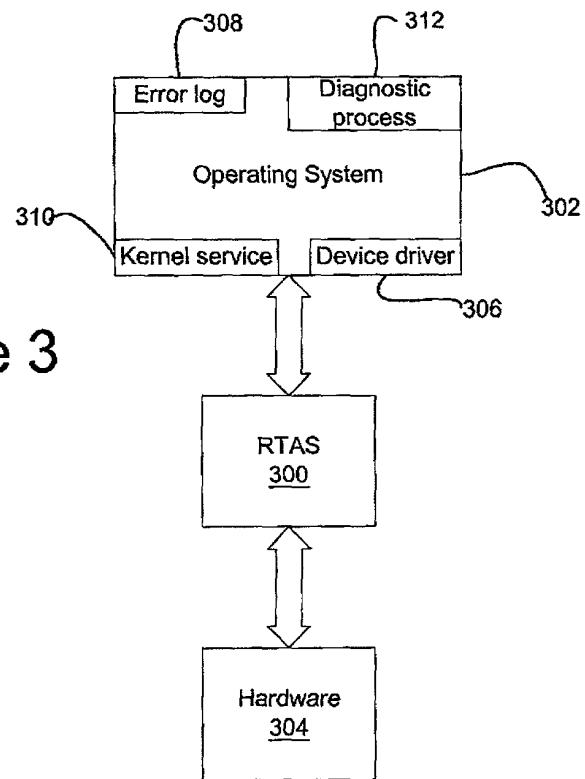


Figure 3

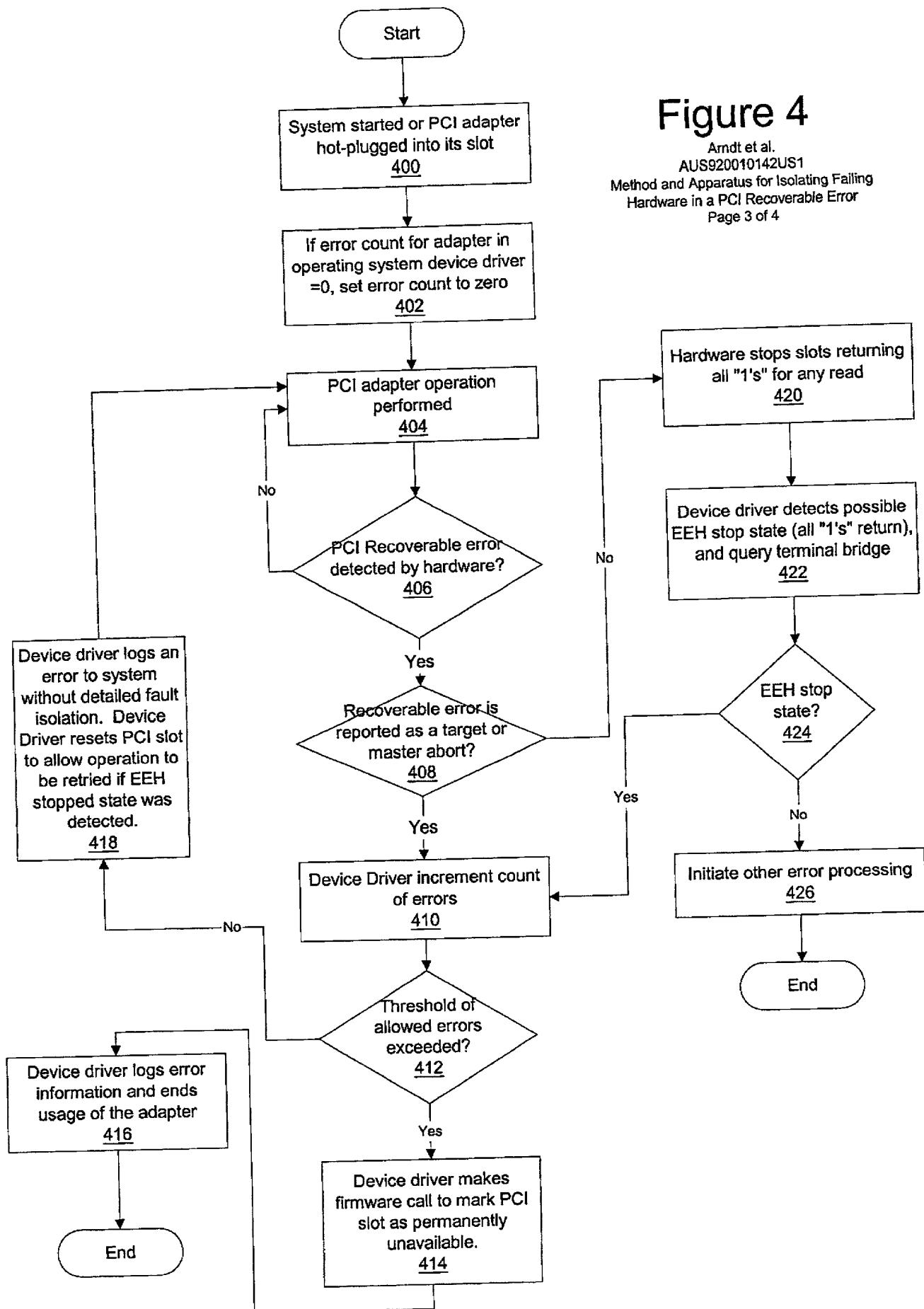


Figure 4

Amdt et al.
AUS20010142US1
Method and Apparatus for Isolating Failing Hardware in a PCI Recoverable Error
Page 3 of 4

Figure 5

Arndt et al.
AUS920010142US1
Method and Apparatus for Isolating Failing
Hardware in a PCI Recoverable Error
Page 4 of 4

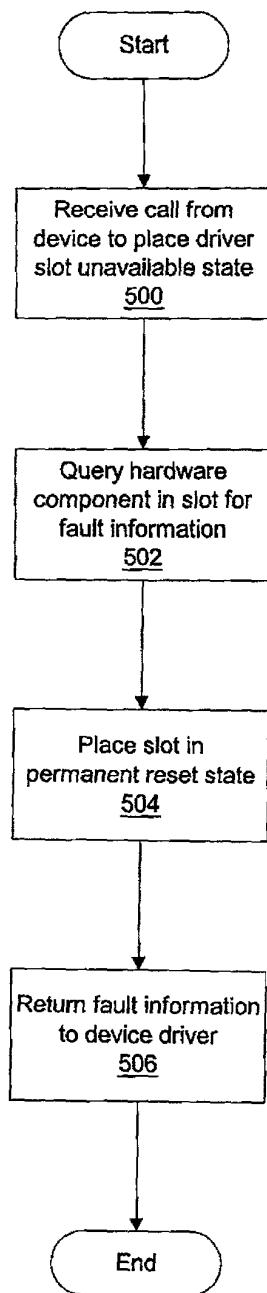


Figure 6

